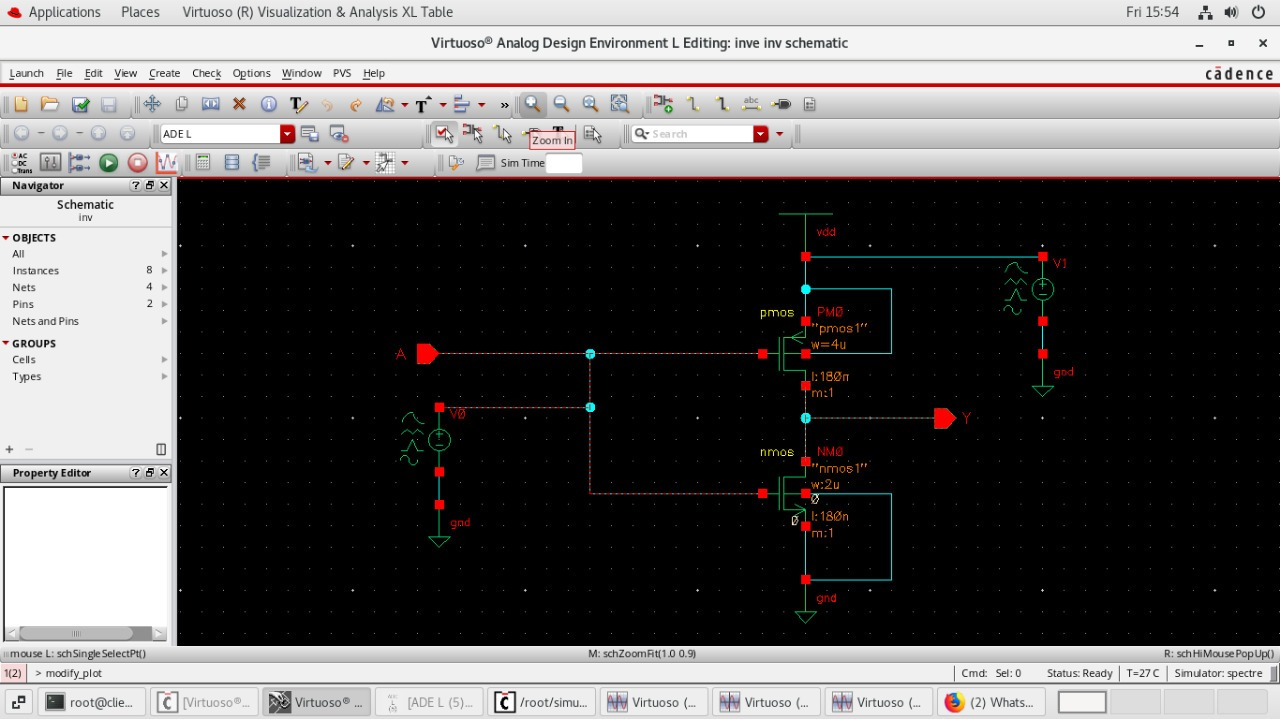
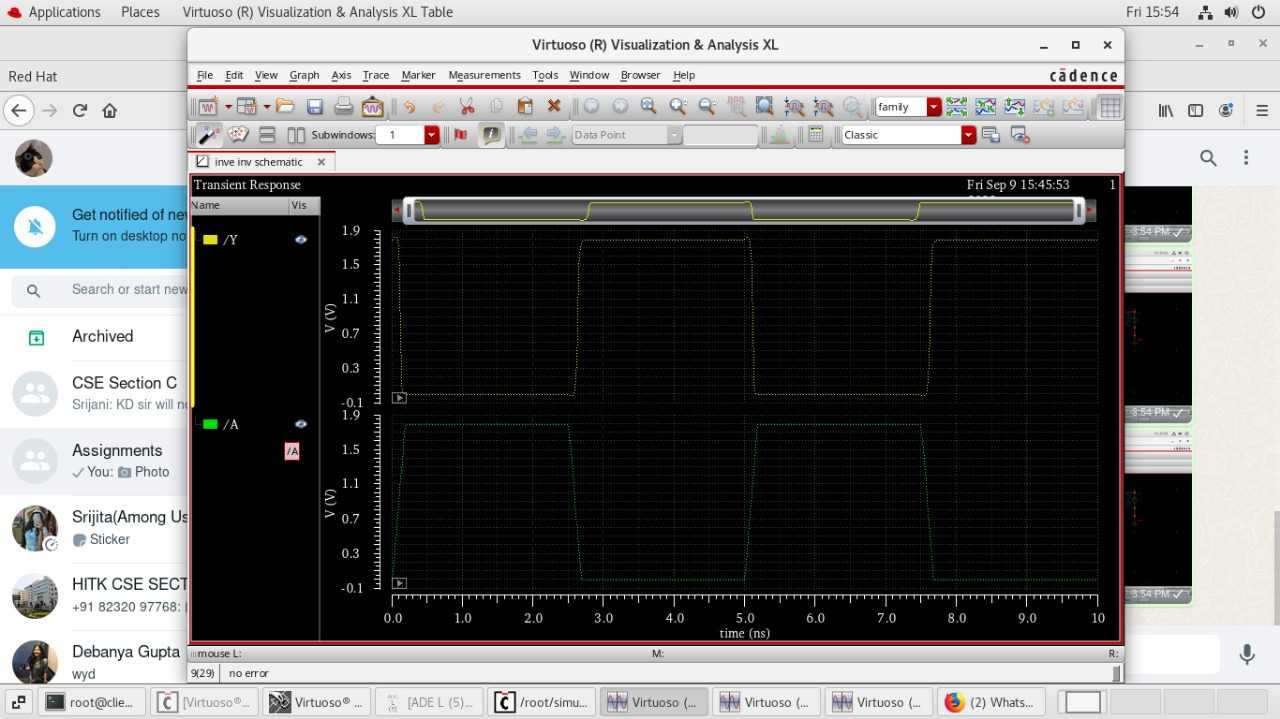
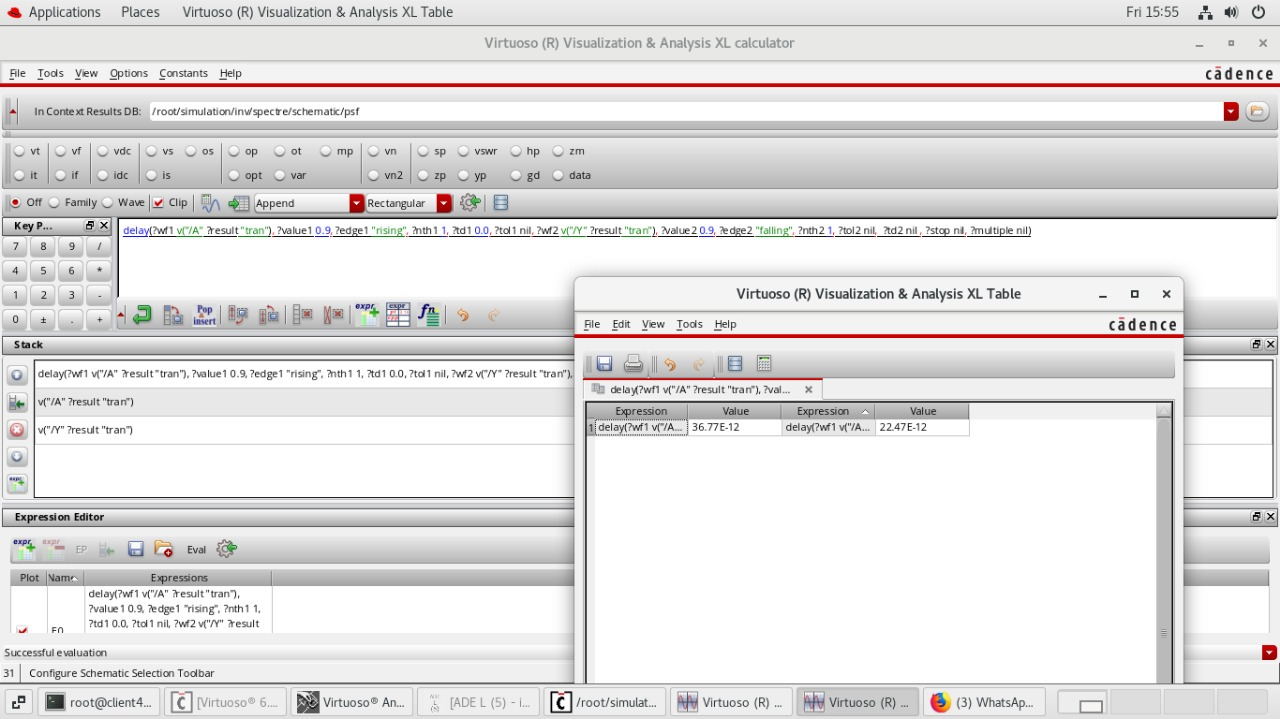
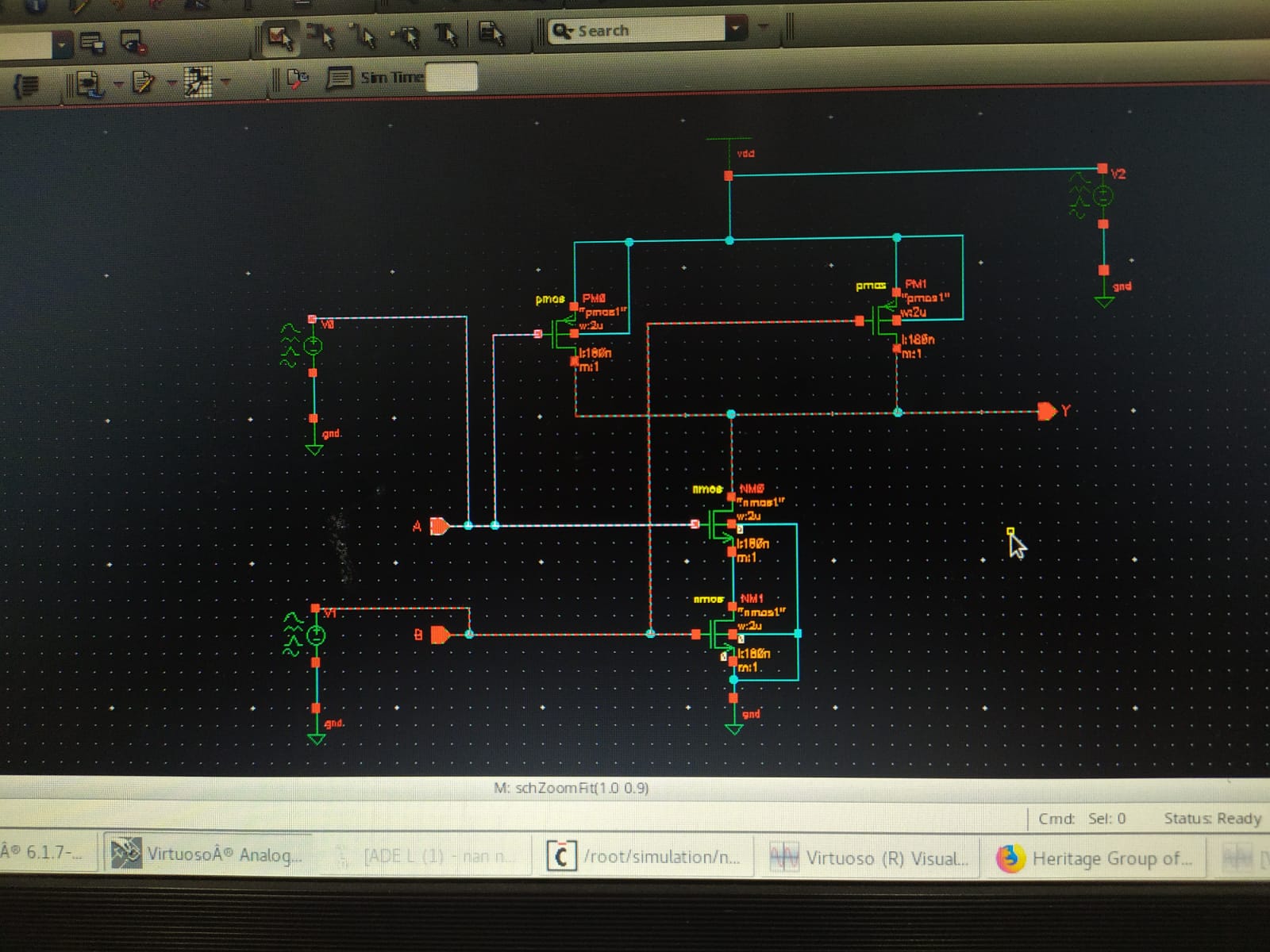
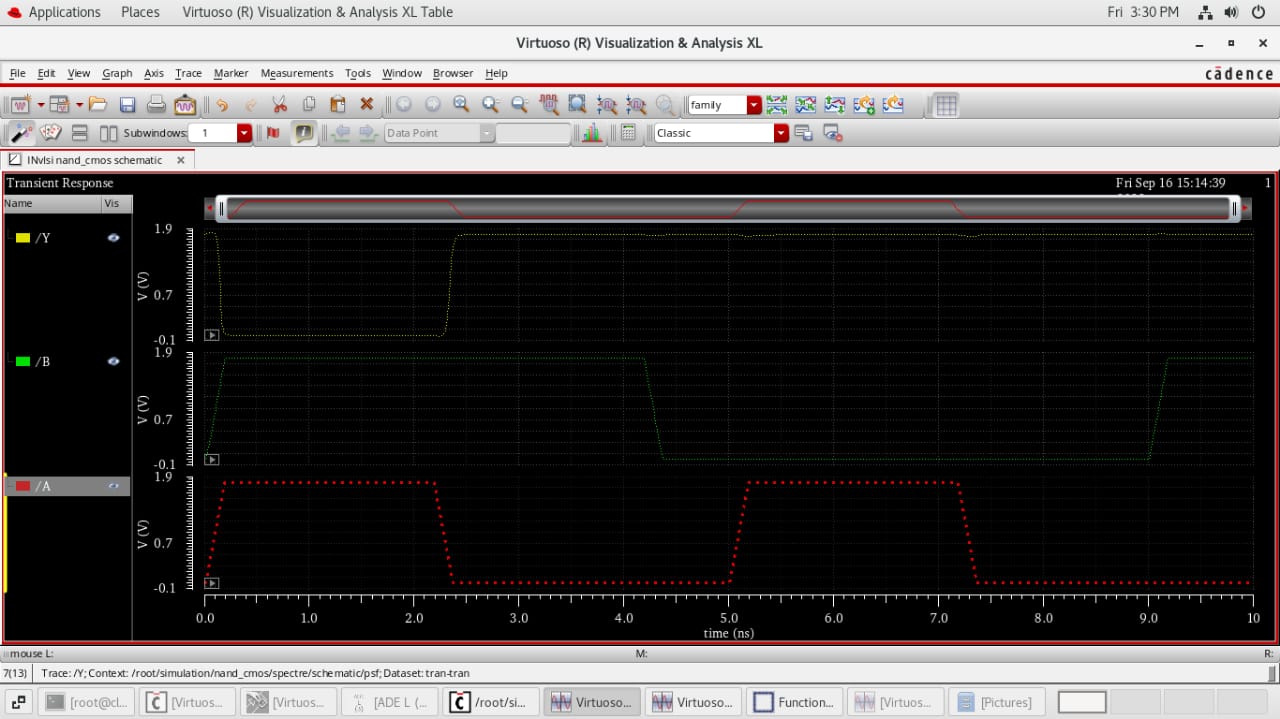
**CMOS INVERTER**

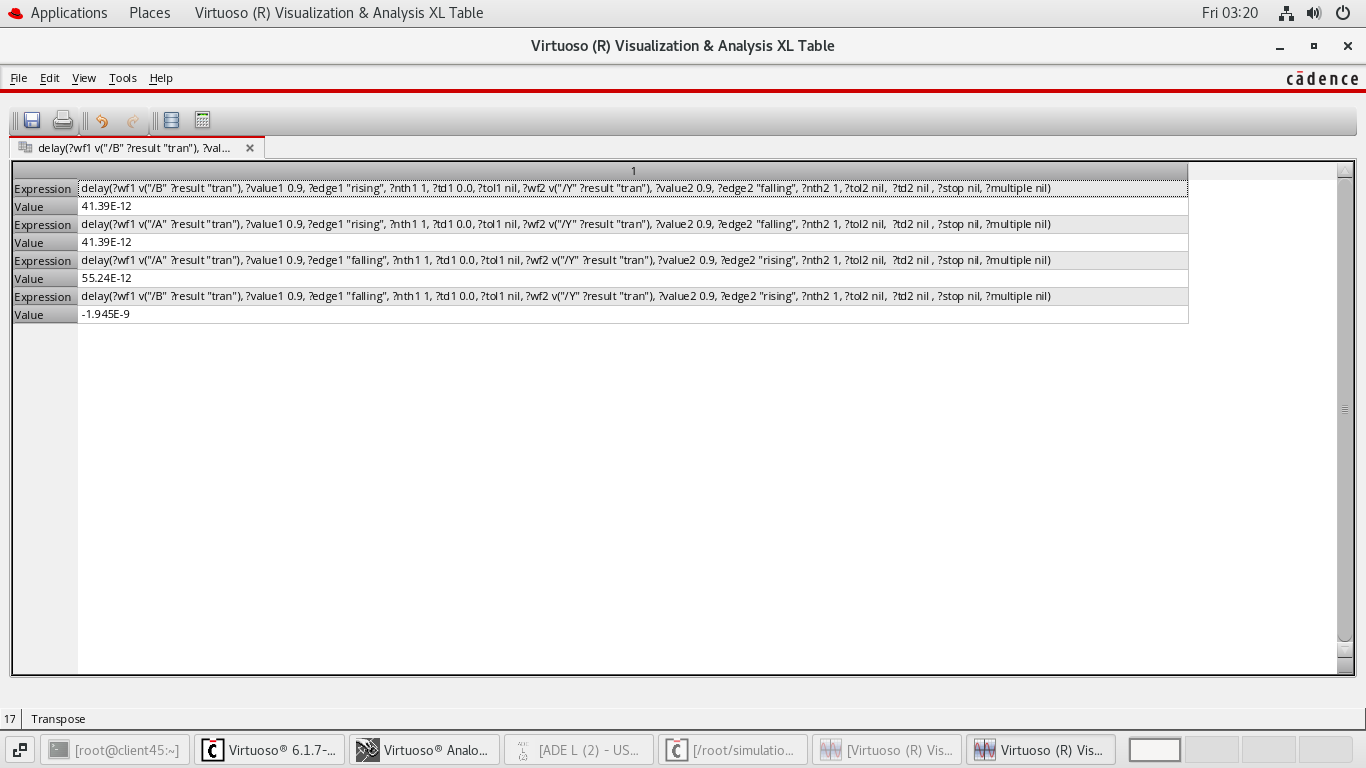




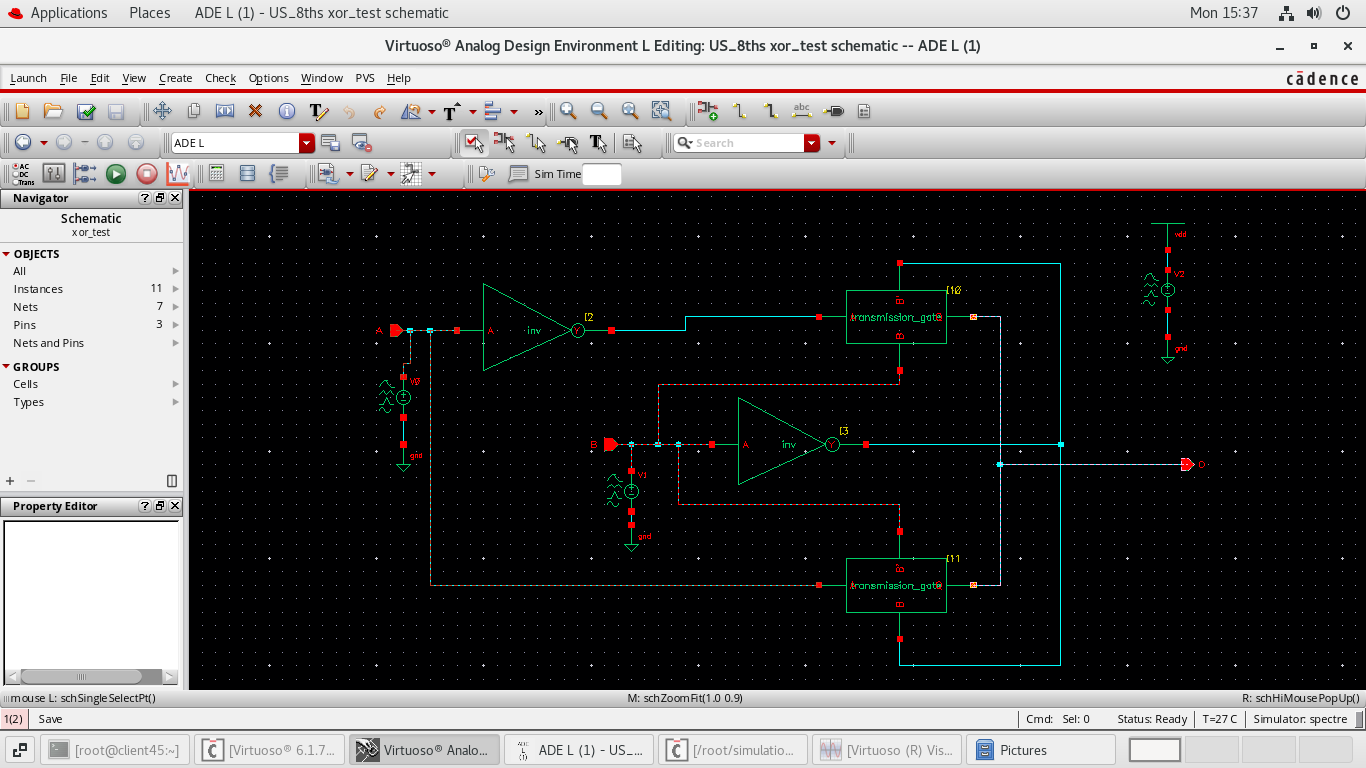
**CMOS NAND GATE**

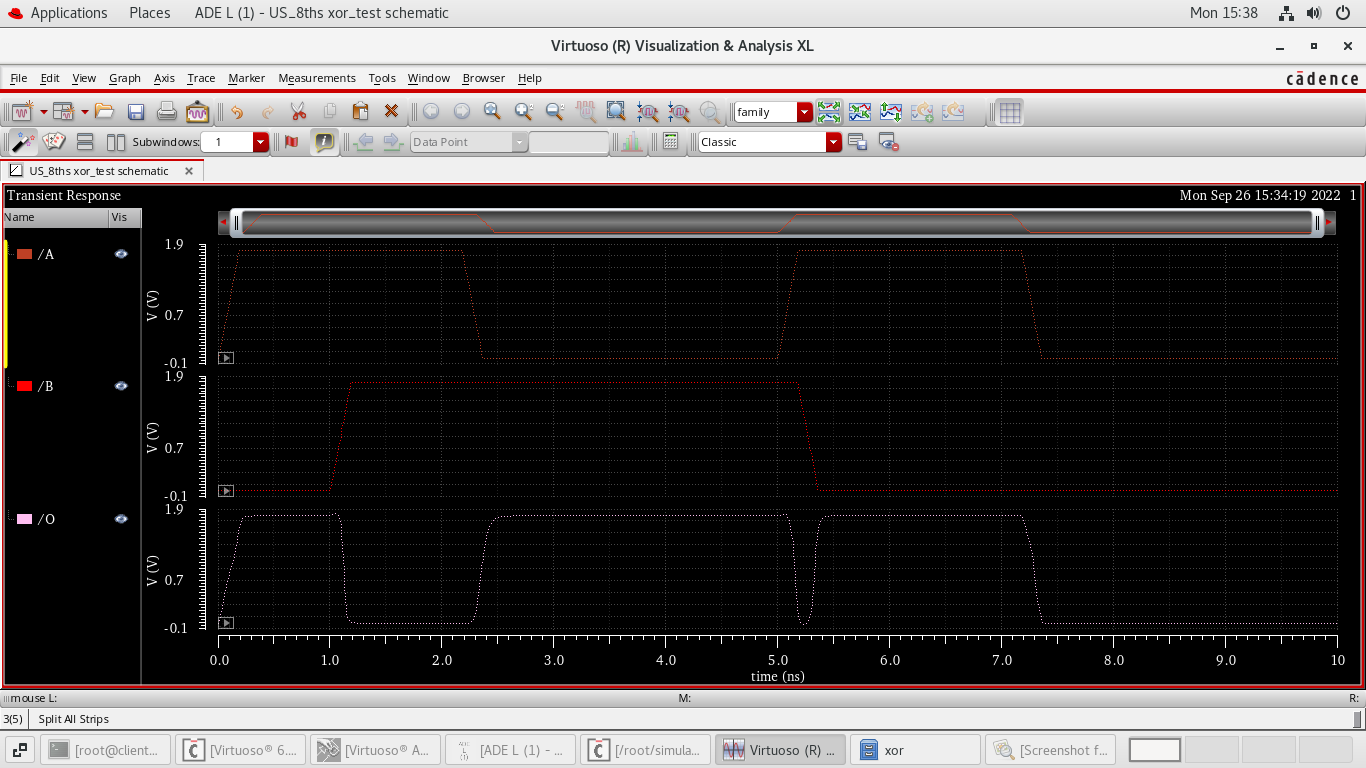




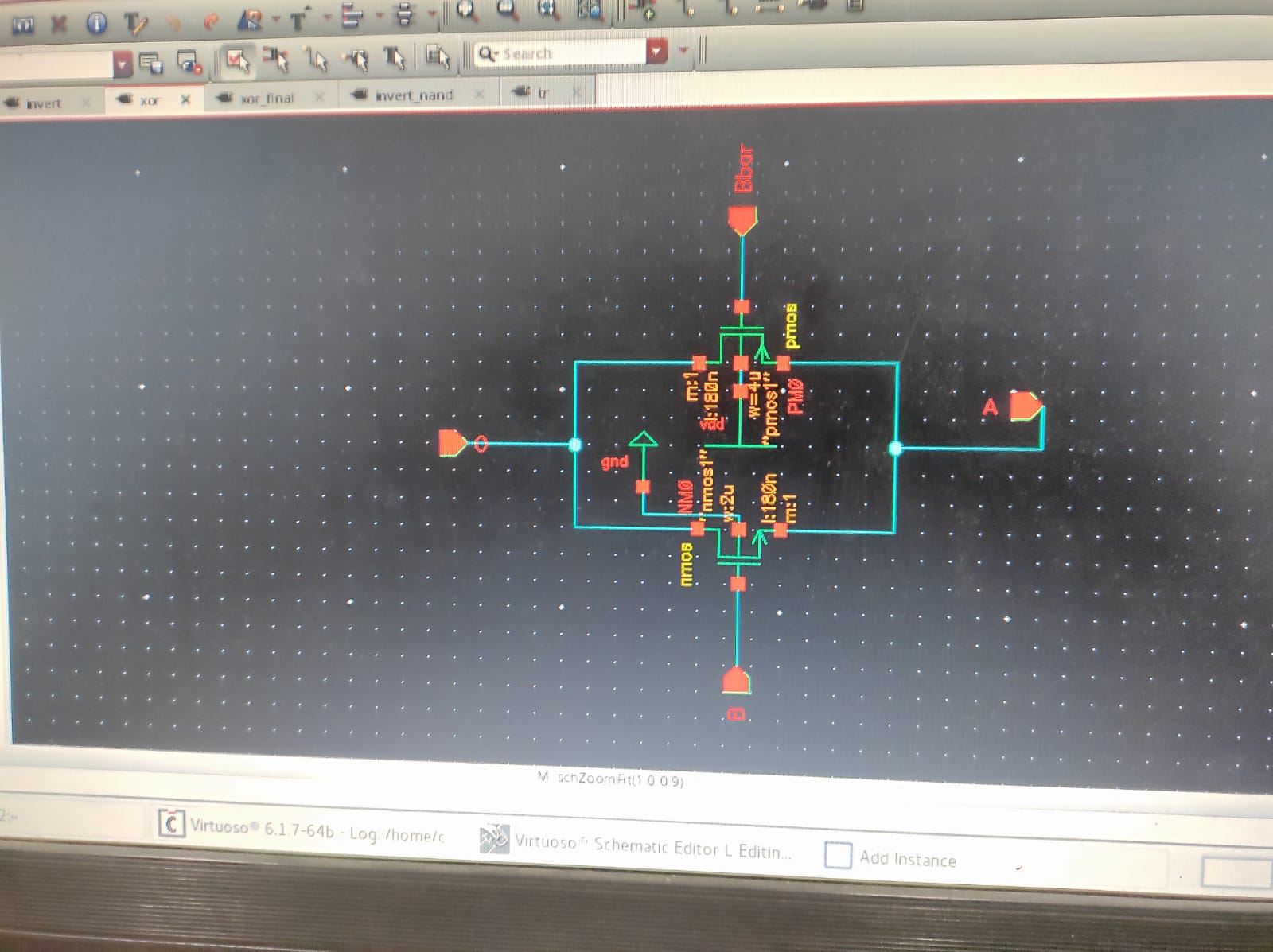


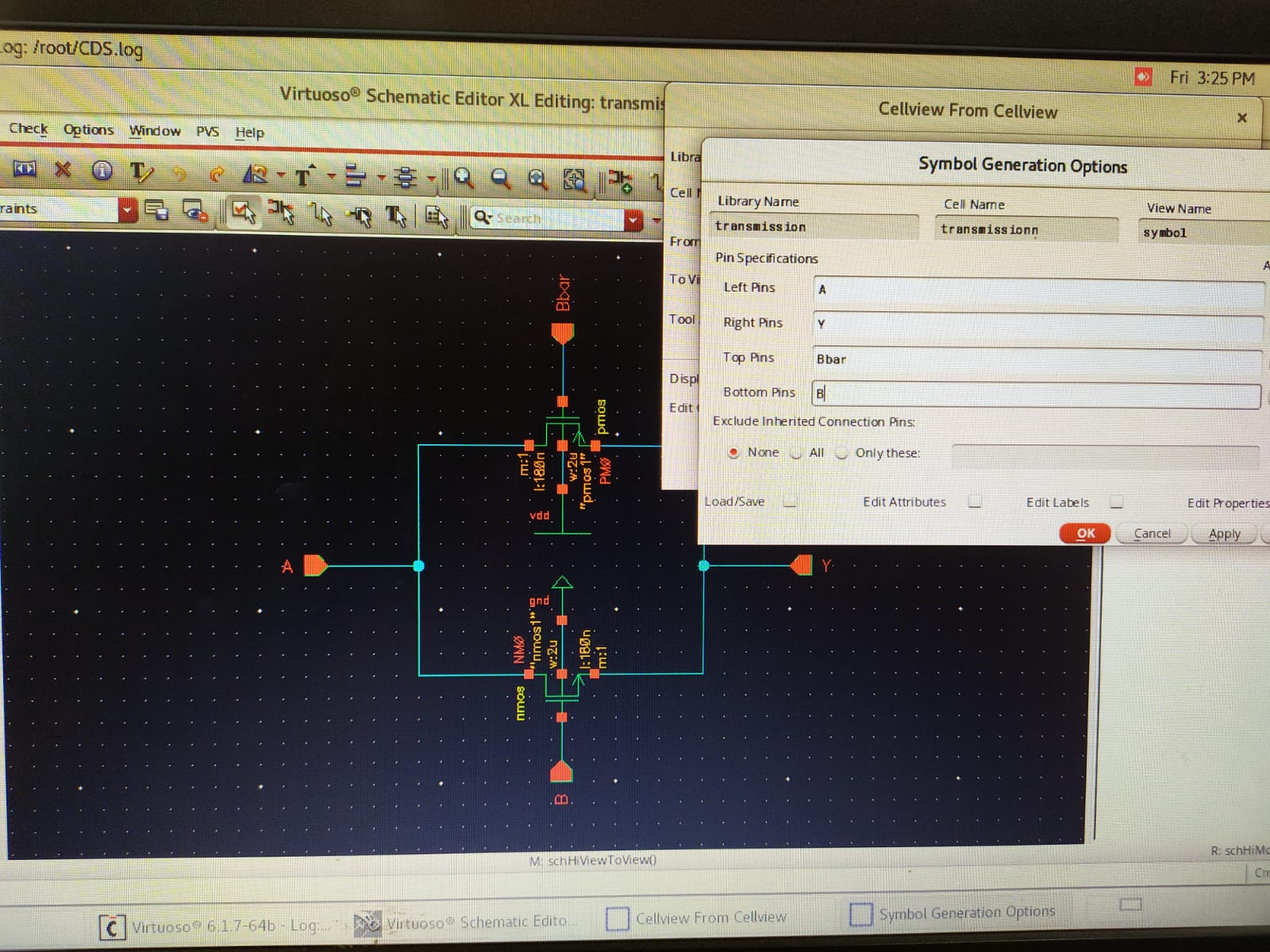
**TRANSMISSION GATE**

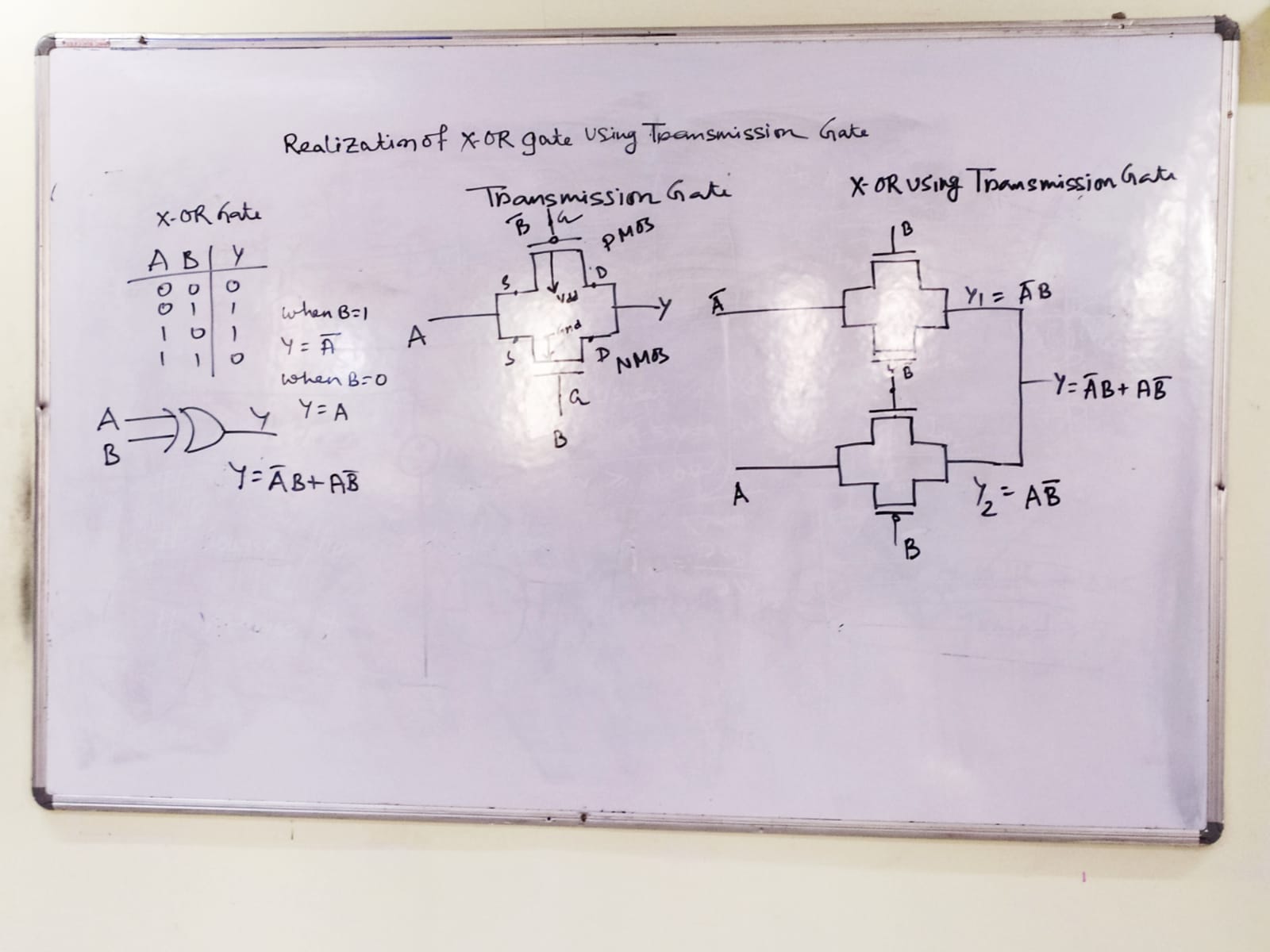




**CMOS XOR**

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